

INFORMATION DISCLOSURE CITATION*(Use several sheets if necessary)*

Docket Number (Optional)

BUR920000229US2

Application Number

Applicant(s)

BUFFET, et al.

Filing Date

Group Art Unit

U.S. PATENT DOCUMENTS

| *EXAMINER INITIAL | REF | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
|----------------------|-----|-----------------|------------|------------------|-------|----------|-------------------------------|
| | | 5,757,816 | 05/26/1998 | Al-Assadi et al. | | | |
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FOREIGN PATENT DOCUMENTS

| | REF | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | Translation | |
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OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

| | | |
|--|--|--|
| | | "Quiescent Signal Analysis for IC Diagnosis", ITC Workshop, 10/5/-10/6 2000. James F. Plusquellic, Chintan Patel and Ying Ouyang, Department of Computer Science & Electrical Engineering, University of Maryland, Baltimore County. |
| | | |

EXAMINER**DATE CONSIDERED**

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.